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ABSTRACT OF THE DISCLOSURE

A synchronized output speech synthesizer having a first memory, a speech synthesizer, a second memory, a multiplexer and a latch device. The speech data of the first memory is read by the speech synthesizer and simultaneously, the latch device through the multiplexer reads the signal data of the second memory. The speech synthesizer and latch device output the voice and status signal in synchrony, while the speaker and the latch device simultaneously output the speech data and the status signals. Therefore, synchronized output of voice and status signals can be carried out, and under conditions of uninterrupted voice synthesis, synchronized status signals can be outputted, so that voice can be continuously and smoothly outputted.